

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	1526	(lcd or liquid adj crystal adj display) and (pixel adj circuit) (static adj memory or SRAM) and (first adj multiplexer) and (demultiplexer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:49
L3	1460	(lcd or liquid adj crystal adj display) and ((pixel adj circuit) (static adj memory or SRAM) and (first adj multiplexer) and (demultiplexer))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:49
L4	1459	(lcd or liquid adj crystal adj display) and ((pixel adj circuit) (static adj memory or SRAM) near (first adj multiplexer) near (demultiplexer))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:50
L5	0	(lcd or liquid adj crystal adj display) and ((pixel adj circuit) near (static adj memory or SRAM) near (first adj multiplexer) near (demultiplexer))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:50
L6	0	(lcd or liquid adj crystal adj display) and ((pixel adj circuit) and (static adj memory or SRAM) near (first adj multiplexer) near (demultiplexer))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:50
L7	0	((pixel adj circuit) and (static adj memory or SRAM) near (first adj multiplexer) near (demultiplexer))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:51
L8	3475	(pixel adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 15:00
L9	64981	(static adj memory or SRAM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:56

L10	6986	(first adj multiplexer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:52
L11	38120	(demultiplexer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:51
L12	28	8 with 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:53
L13	2	8 with 10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:52
L14	17	8 with 11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:53
L15	10	8 and 10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:52
L16	230	(multiplexer) and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:52
L17	28	(multiplexer) with 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:52

L18	146	8 and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:53
L19	59	8 and 11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:53
L20	35	16 and 18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:55
L21	1	19 and 20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:53
L22	203	(static adj memory or SRAM or memory) with 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:57
L23	15	16 and 22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:57
L24	395895	(lcd or liquid adj crystal adj display)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:57
L25	8	23 and 24	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:59

L26	1	11 and 25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:59
L27	2	10 and 25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:59
L28	0	(pixel adj circuit) with (multiplexer near4 demultiplexer near4 memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 15:01
L29	1	(pixel adj circuit) with (multiplexer with demultiplexer with memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 15:01
L30	1	(pixel adj circuit) with (multiplexer same demultiplexer same memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 15:01
L31	134	((pixel or switch\$3) adj (circuit or device or system)) same (multiplexer same demultiplexer same memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 15:02
L32	134	((pixel\$1 or switch\$3) adj (circuit or device or system)) same (multiplexer same demultiplexer same memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 15:02
L33	2	24 and 32	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 15:04

L34	107415	(tft or thin adj film adj transistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 15:04
L35	1	32 and 34	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 15:05
L36	2	33 and 24	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 15:05

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(lcd or liquid adj crystal adj display) and (pixel adj circuit) with (static adj memory or SRAM) with (lowering adj power adj consumption) with (A/D OR D/A) and (first adj multiplexer) with (plurality adj current adj switching adj elements) and (demultiplexer) with (plurality adj current adj switching adj elements) and (static adj memory) adj ((scannin adj line) with (first adj multiplexer) with (demultiplexer)) and (tft or thin adj film adj transistor) adj ((scanning adj line) with (data adj line) and (capacitor) adj ((tft or thin adj film adj transistor) with (D/A or A/D)) and (switch) adj ((first adj multiplexer) with demultiplexer with (liquid adj crystal adj unit))).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/27 14:46